

25th August, 1996

BULLETIN #137

SYNCHRO-PULSE CDT CP38**PCB CP38-11****DISPLAY RESETTING FAULT**

The Development Department has advised the following modification to the pcb assembly CP38-11. The change has been extensively tested, and shown to reliably overcome the problem of display resetting. The change modifies the way in which the circuitry deals with changing power supply voltage, particularly at power on and power off.

Modification steps:

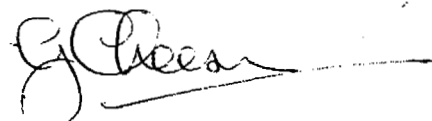
- On the underside of the CP38-11 pcb, cut the track from pin 2 of U11 to C16;
- Solder a 10 ohm resistor from pin 2 of U11 to C16 (in place of the track);
- Solder a 470 ohm resistor from pin 3 of U11 to pin 9 of U11.

These steps are illustrated in the attached diagrams.

Please note that while the above changes have been implemented in our Production department, in the longer term the same result will be achieved through use of an alternative NVRAM device CP38-11/5. The presently used MK48Z02 will be replaced with any of the following; MK48Z12, M48Z12, DS1220AD or DS12220Y.

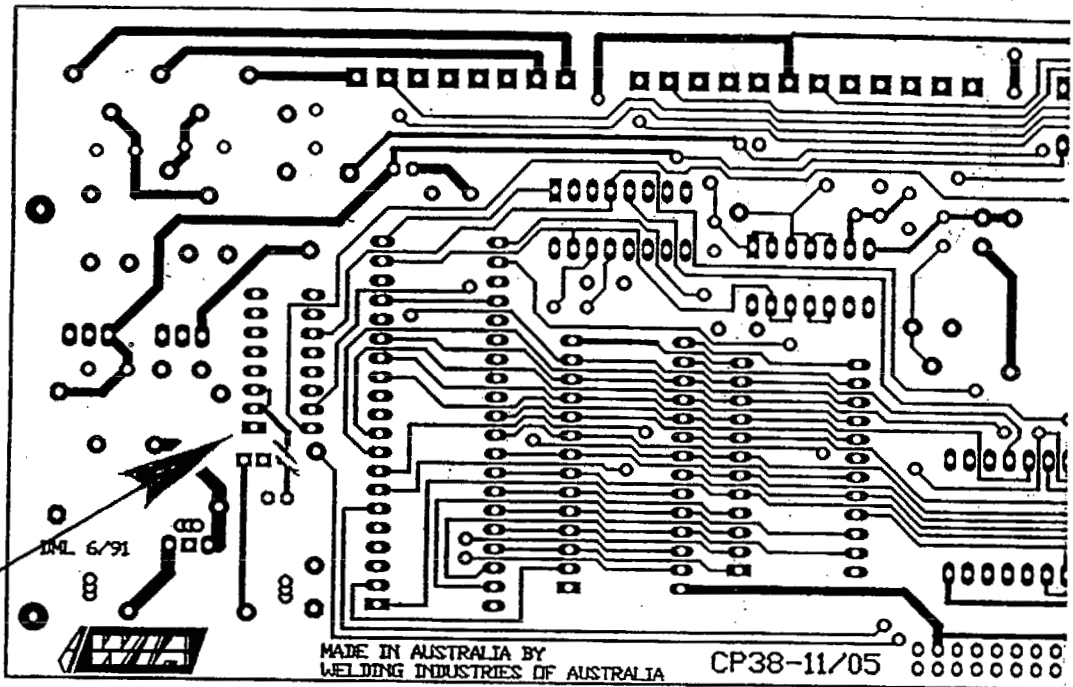
Thanks to David Leske for tracking down this difficult problem.

Equipment Product Manager

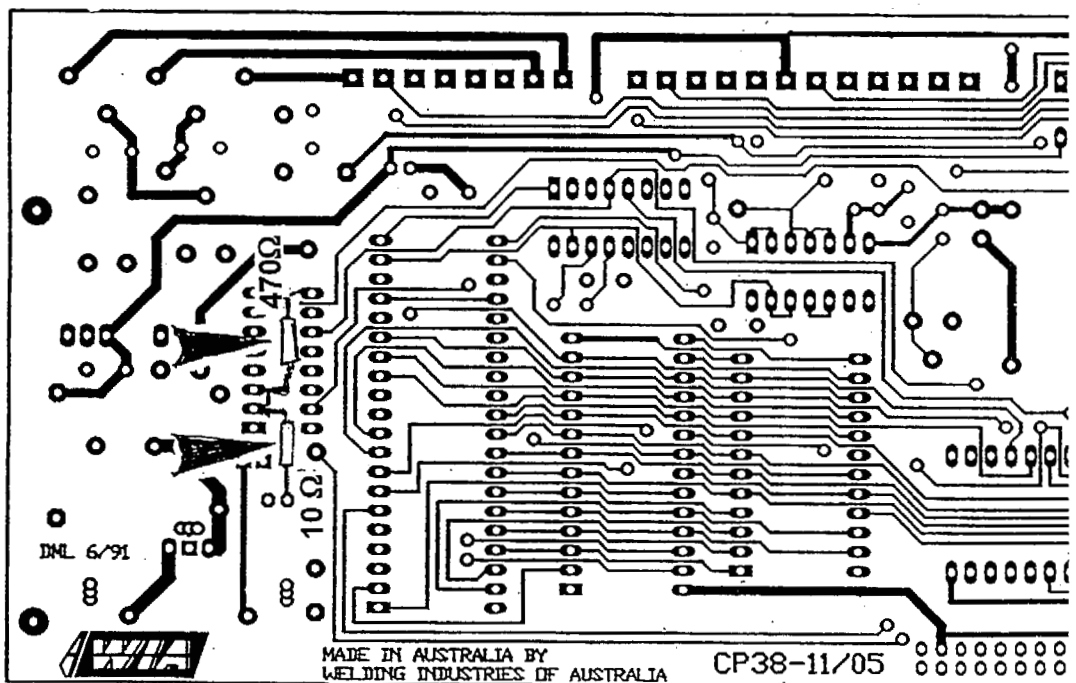


Gary Cheesman

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Cut track (on solder side) from pin 2 of U11 to C16



Add 470 ohm resistor from pin 3 of U11 to pin 9 of U11
Add 10 ohm resistor from pin 2 of U11 to C16